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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application:

Hideki IWAKI et al.

Group Art Unit: 2841

Serial No.: New Divisional Application

Examiner: Q. Vu

Filed: November 28, 2001

For: MULTI-LEVEL CIRCUIT SUBSTRATE, METHOD FOR MANUFACTURING SAME
AND METHOD FOR ADJUSTING A CHARACTERISTIC IMPEDANCE THEREFOR

Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to initial examination, please amend the above-identified patent application as

follows.

IN THE CLAIMS:

Please cancel claims 12-14 without prejudice or disclaimer.

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